

**AMENDMENTS TO THE CLAIMS**

**This listing of claims will replace all prior versions and listings of claims in the application:**

**LISTING OF CLAIMS:**

1. (currently amended): A method for the production of gallium nitride compound semiconductor chips from a wafer having gallium nitride compound semiconductor layers laminated on a principal surface of a substrate formed of hexagonal crystal, comprising:

a step of linearly forming first grooves in a desired chip shape by etching on a side of the gallium nitride compound semiconductor layers of said wafer,

a step of forming second grooves having a line width (W2) equal to or smaller than a line width (W1) of the first grooves on a side of the substrate of said wafer at positions not conforming to the central lines of the first grooves, and

a step of dividing said wafer along said first and second grooves into pieces each of a chip shape,

wherein said positions at which the second grooves are formed are decided by performing a trial division in advance.

2. (previously presented): A method according to claim 1, wherein said substrate is formed of sapphire, with a C surface of the sapphire substrate as the principal surface, said first and second grooves are formed respectively along a first direction parallel to an orientation flat and along a second direction orthogonal to said first direction, and the wafer is divided along the first and second grooves.

3. (previously presented): A method according to claim 1, wherein the positions not conforming to the central lines of said first grooves are, when viewing the substrate in plan view, positions parted by 20 to 100% of the line width (W1) of the first grooves relative to the central lines of the first grooves.

4. (previously presented): A method according to claim 1, wherein at the step of forming said second grooves, the second grooves are formed so that the obliquely divided chips assume cut faces having angles in the range of 60 to 85°.

5. (previously presented): A method according to claim 1, further comprising a step of polishing the substrate side prior to forming the second grooves to adjust a thickness of the substrate in a range of 60 to 100  $\mu\text{m}$ .

6. (previously presented): A method according to claim 1, wherein said first grooves are confronted by an electrode-forming surface for forming an electrode for gallium nitride compound semiconductor chips.

7. (previously presented): A method according to claim 1; wherein said second grooves are formed by at least one method selected from the group consisting of etching, dicing, pulse laser and scribe.

8. (previously presented): A method according to claim 1, wherein said substrate is formed of hexagonal SiC.

9. (previously presented): A method according to claim 1, wherein said substrate is formed of a hexagonal nitride semiconductor.

10. (previously presented): A method according to claim 1, wherein said substrate is formed of hexagonal GaN.

11. (previously presented): Semiconductor chips obtained by the method for the production of semiconductor chips set forth in claim 1.